

Monsalvat

PRE-2



Preliminary Datasheet

Verity Audio's Monsalvat Pre-2 digital preamplifier is directly derived from our Monsalvat's breathtaking Pro-6, sharing the same topology, quality parts and configuration. However, it's built into a single chassis that includes a power supply.

The Pre-2 combines the functions of a preamplifier and a DAC. Its design gives no ground to compromise, as per Verity's traditional philosophy. The preamplifier's architecture features state-of-the-art ADC/DAC circuitry and total galvanic isolation between the analog and digital domains, ensuring they remain perfectly isolated. The Pre-2 reaches the highest performance a "pure analog" preamplifier can attain while still offering the advantages you would expect of a reference digital signal processors.

The Pre-2 includes both analog and digital inputs. Analog inputs are digitized at a very high sampling rate and fed into the digital domain for processing, while digital inputs are processed directly. The results of digital processing are then transferred to the analog domain to be reproduced with perfect integrity.



The chassis of the preamplifier is built of thick aluminum plates and is supported by a massive Verity anti-vibration platform.

The result is an astonishing ultra-low-noise preamplifier offering pure sound transparency and fluidity, an extended bandwidth and an ultra-high dynamic range. The new Verity Pre-2 redefines preamplification thanks to the advanced technologies available today.

ANALOG DOMAIN

- Each analog output channel is completely independent and is driven by 8 parallel 32-bit DACs powered by 6 dedicated ultra-low noise power supplies.
- Each analog input pair features a dedicated 32-bit ADC running at 384k samples per second and is powered by 5 dedicated ultra-low-noise power supplies.
- The analog and digital domains are separated through high-performance, high-speed galvanic digital isolators.
- A total of 27 ultra-low-noise power supplies power the analog domain.

DIGITAL DOMAIN

- The digital domain is centered on a powerful high-speed FPGA capable of 800+ billion multiply-accumulate operations per second (800+ GMAC/s).
- The 32-bit processing architecture (up to 72 bits internal calculations @ 98 MHz), enables compromise-free digital signal processing and superb digital filters featuring perfect characteristics and an ultra-low noise floor.
- 2 separate ultra-low-noise power supplies power the digital domain.

INTERFACES

Analog Outputs

- Balanced outputs: 1 stereo pair of male XLR connectors.
- Unbalanced outputs: 1 stereo pair of RCA phono connectors.

Analog Inputs

- Balanced inputs: 1 stereo pair of female XLR connectors.
- Unbalanced inputs: 1 stereo pair of RCA phono connectors.

Digital Inputs

- 1 USB 2.0 input accepts PCM data up to 32 bits at 44.1, 48, 88.2, 96, 176.4, 192, 352.8 and 384 kS/s and DSD at 2.822 MHz, 3.072 MHz, 5.644 MHz and 6.144 MHz.
- 1 AES/EBU on female XLR connector accepts up to 24-bit PCM at 44.1, 48, 88.2, 96, 176.4 and 192 kS/s.
- 1 SPDIF on RCA phono connectors accepts up to 24-bit PCM at 32, 44.1, 48, 88.2, 96, 176.4 and 192 kS/s.
- 1 I²S proprietary digital interface.

Chassis

- The power supply requires 100-120 or 220-240VAC (set at the factory). Maximum power consumption is 60 watts.
- Dimensions: (W x D x H) 430 mm x 360 mm x 150 mm.
- Weight: 24 kg.